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2823

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q56320

Kaoru NARITA

Appln. No.: 09/421,273

Group Art Unit: 2823

Confirmation No.: 3894

Examiner: J. GARCIA

Filed: October 20, 1999

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TECHNOLOGY CENTER 2800

For: SEMICONDUCTOR DEVICE HAVING PROTECTION CIRCUIT IMPLEMENTED BY  
BIPOLAR TRANSISTOR FOR DISCHARGING STATIC CHARGE CURRENT AND  
PROCESS OF FABRICATION

**AMENDMENT UNDER 37 C.F.R. § 1.111**

**MAIL STOP NON-FEE AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Non-Final Office Action dated June 20, 2003, please amend the above-identified application as follows on the accompanying pages.

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